

# C.V

**Khaola Kasas, PhD.**

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## Education

PhD. Degree in Communication Engineering, Damascus University, Syria 08/2008 to 2/2012.

Master Degree in Real-Time Electronic Systems, Bradford University, England.  
09/1993 to 12/1994.

Bachelor's Degree of Electronic Engineer, Damascus University, Syria 1973-1978

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## Employment History

Employment History		
<b>Date:</b> 2016-up to date	Qasyoun Privete University QPU, Engineering College	Damascus
<b>Lecturer</b> <ul style="list-style-type: none"><li>- Signals &amp; System analysis</li><li>- Analog &amp; Digital Communication Systems.</li><li>- Digital System Design FPGA</li><li>- Real Time system Design (Radar Systems...</li><li>- Digital Signal Processing</li><li>- Fundamentals of Electric Circuit I, II</li><li>- Electronic Devices Circuits I, II</li><li>- Micro Processing and Microcontrollers</li></ul>		
<b>Date:</b> 2009-2015	<b>Institute:</b> SSRC(Center Of Scientific Studies and Research) Syria & HIAST(Higher Institute for Applied Science & Technology ) syria	Damascus
<b>Researcher</b> <ul style="list-style-type: none"><li>• Supervisor team projects working in real time system design for Industrial Applications.</li><li>• Improving &amp; preparing new graduates engineers for the work and for research.</li></ul>		

## **Responsibilities**

- Design, implement and test electronic systems & subsystems.
- Head of team of designer working on Industrial Application.
- Supervision and oversight for the manufacturing of the new systems..
- Projects management.
- Supervise the undergraduate Communication Eng. student's projects at Damascus University, HIAST and at QPU (from 2015-2022)

## **Experiences**

- Design & implementation Systems.(hardware)
- Design & implementation Testing Systems. .(hardware, software & using PC )
- Assembly & C For Programming MCs AVR family (hardware &software)
- Design ASICs Circuits (FPGA using Quartus tools ), VHDL language
- Design & implement DSP Communication system Using FPGA and using software tools Matlab.
- Design & implement analog and digital circuit.

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## **Personal data**

- Date of birth: 06/05/1955
- Marital status: Married
- Nationality: Syrian

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## **Papers**

- Improving Ambiguity Function of Costas signal (Microwave Symposium (MMS), 2009 Mediterranean, Kasas - Aboulmour- Kawas)
- Improving Costas Radar Signal by Applying Variable Time Spacing Using Costas Array And Golomb Ruler (Damascus University Journal Vol. (26) - No. (2) 2010 Kasas - Aboulmour- Kawas)
- Studying the effect of variable time spacing on Orthogonal Costas Frequency Modulation Pulses .(Damascus University Journal Vol. (27) - No. (2) 2011 Kasas- Aboulmour- Kawas).

## **Reference**

All references are available upon request.

<http://www.damascusuniversity.edu.sy/mag/eng/images/stories/21-32.pdf>

<http://www.damascusuniversity.edu.sy/mag/eng/images/stories/43-.44.pdf>

Improving Ampiguity Function of costas signal (Microwave Symposium MMS 2009, Kasas-Aboulmour- Kawas.

**Khaola Kasas 1-2023**